

Fig. 1: Schematic cross-section of microseismometer

n - ~9

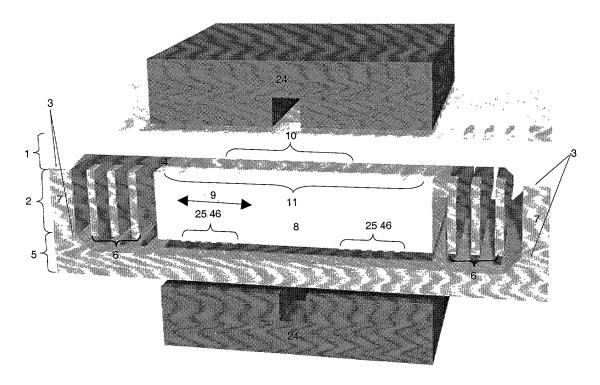
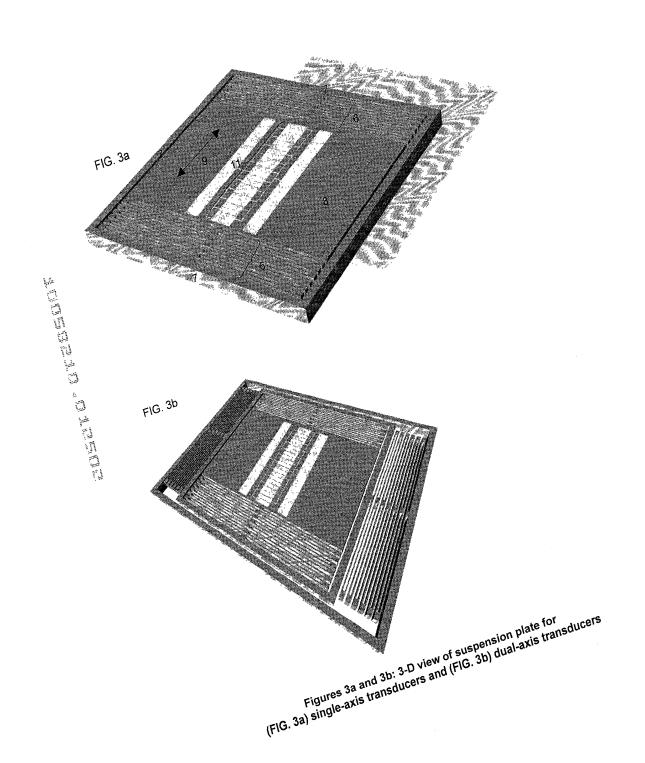


Fig. 2: 3-D View of microseismometer



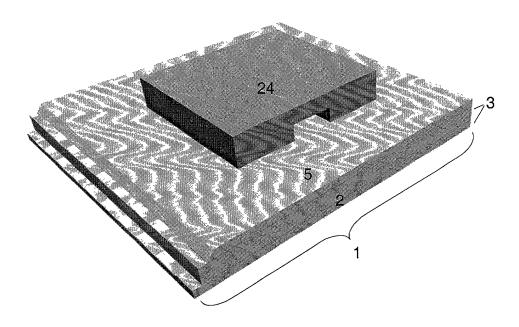
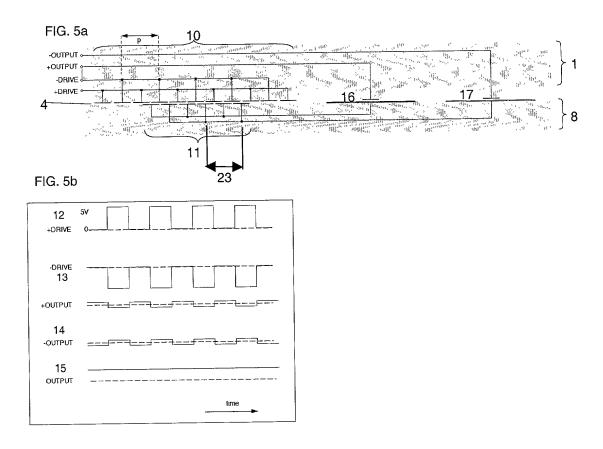
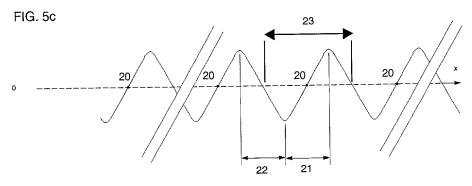
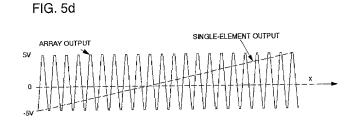


FIG. 4: Wafer stack with magnet, shown inverted compared to other figures.







Figures 5a, 5b, 5c and 5d: Electronics schematic

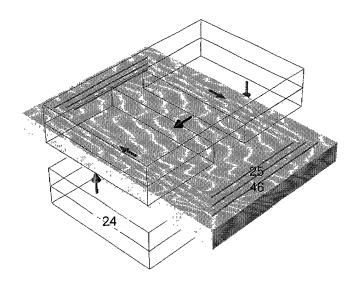


FIG. 6: Magnetic circuit

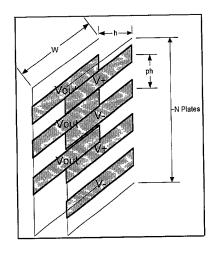
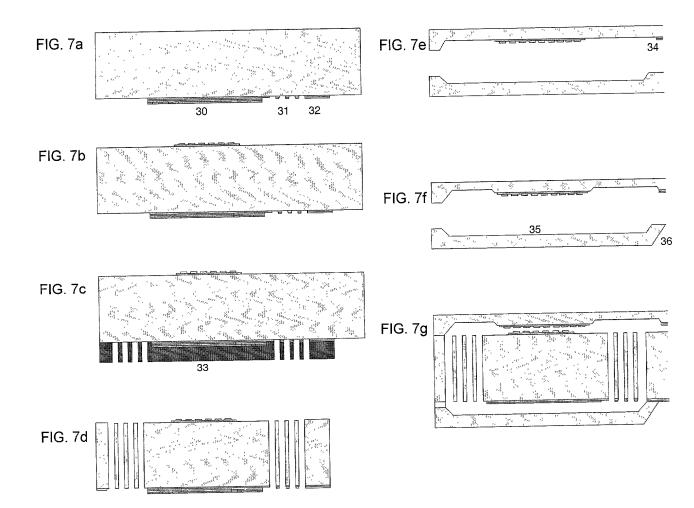
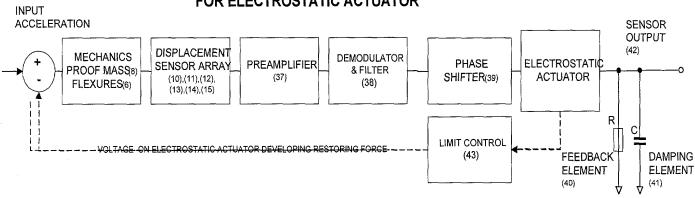


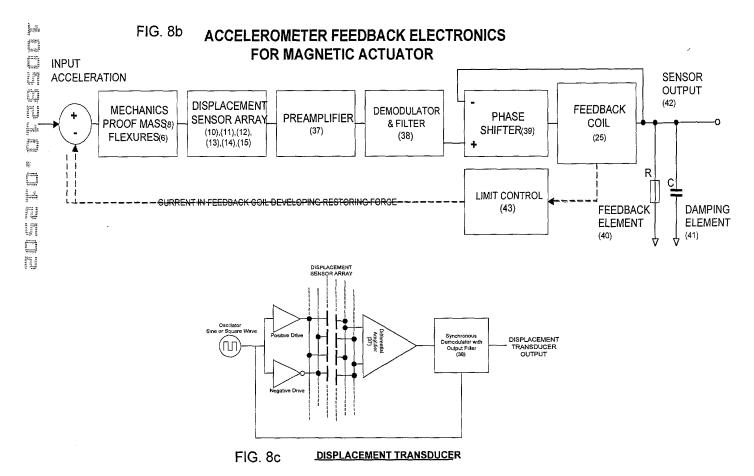
FIG. 10



Figures 7a, 7b, 7c, 7d, 7e, 7f and 7g: Fabrication

FIG. 8a ACCELEROMETER FEEDBACK ELECTRONICS FOR ELECTROSTATIC ACTUATOR





Figures 8a, 8b and 8c: Accelerometer Feedback Circuits

FIG. 9: Seismometer Feedback Electronics

